

**Notice of References Cited**

Application/Control No.

09/818,951

Applicant(s)/Patent Under  
Reexamination  
SOLOMON, GARY A.

Examiner

Donna K. Mason

Art Unit

2181

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,860,150	01-1999	Chiarot et al.	711/213
	B	US-6,085,291	07-2000	Hicks et al.	711/137
	C	US-6,167,506	12-2000	Witt, David B.	712/213
	D	US-5,765,213	06-1998	Ofer, Erez	711/213
	E	US-6,609,168	08-2003	Willke, II, Theodore L.	710/110
	F	US-6,134,643	10-2000	Kedem et al.	711/213
	G	US-6,195,735	02-2001	Krueger et al.	711/204
	H	US-5,146,578	09-1992	Zangenehpour, Saied	711/137
	I	US-5,778,435	07-1998	Berenbaum et al.	711/137
	J	US-6,496,921	12-2002	Arimilli et al.	712/207
	K	US-6,625,696	09-2003	Willke, II, Theodore L.	711/137
	L	US-5,958,040	09-1999	Jouppi, Norman P.	712/207
	M	US-6,401,193	06-2002	Afsar et al.	712/207

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 01189728 A	07-1989	Japan	NONOMURA et al.	G06F 09/38
	O	JP 2000187638 A	07-2000	Japan	YATSUGAYO, MASATAKA	G06F 13/36
	P	EP 284100 A2	09-1988	European Patent	KIMURA, SHINYA C O NEC CO	G06F 09/38
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Bus Request Update Mechanism to Improve Processor Performance," Aug. 1, 1995, IBM Technical Disclosure Bulletin, p. 261 264.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.